

L Number	Hits	Search Text	DB	Time stamp
1	4061	((memory storage) near5 (controller interface)) same (control near4 logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/07/31 18:03
2	789	(multiple near4 processor) with (shar\$3 near5 (memory storage device))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/07/31 18:03
4	93	((memory storage) near5 (controller interface)) same (control near4 logic)) and ((multiple near4 processor) with (shar\$3 near5 (memory storage device)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/07/31 18:09
6	4347	(schedul\$3 arrang\$6) near4 access\$3 near4 (memory storage device)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/07/31 18:06
11	69	((schedul\$3 arrang\$6 plan\$4) and (((memory storage) near5 (controller interface)) same (control near4 logic)) and ((multiple near4 processor) with (shar\$3 near5 (memory storage device)))) and (((memory storage device) adj3 (contrller interface manag\$6)) and (((memory storage) near5 (controller interface)) same (control near4 logic)) and ((multiple near4 processor) with (shar\$3 near5 (memory storage device))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/07/31 18:11